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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10073368	02/13/2002	250	214 R	2878	<i>Spears MonHea</i>

**\*\*APPLICANTS:** Lauffenburger James; Buescher Kevin;

**\*\*CONTINUING DATA VERIFIED:**

**\*\* FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO Q-68549
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		
Verified and Acknowledged Examiners's initials		
TITLE : Low power integrating circuit for use with a photodetector and optical sensor including such an integrating circuit		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>		
<b>ISSUE FEE</b> Amount Due      Date Paid		Assistant Examiner		Total Claims
		Primary Examiner		Print Claim for O.G.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		PREPARED FOR ISSUE		<b>DRAWING</b>
		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.		Sheets Drwg.      Figs. Drwg.      Print Fig.
		Application Examiner		

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